

008260" 4872950

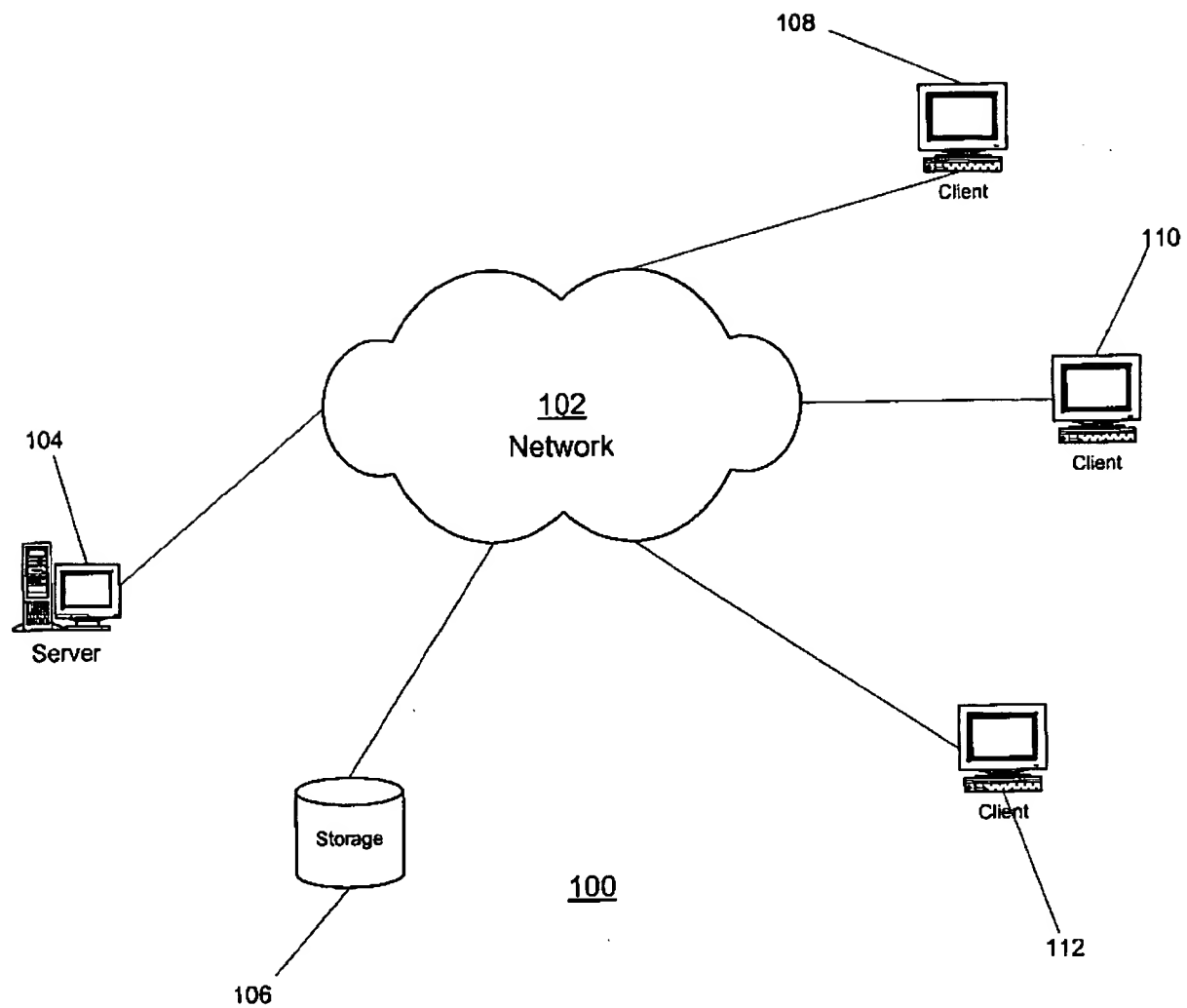
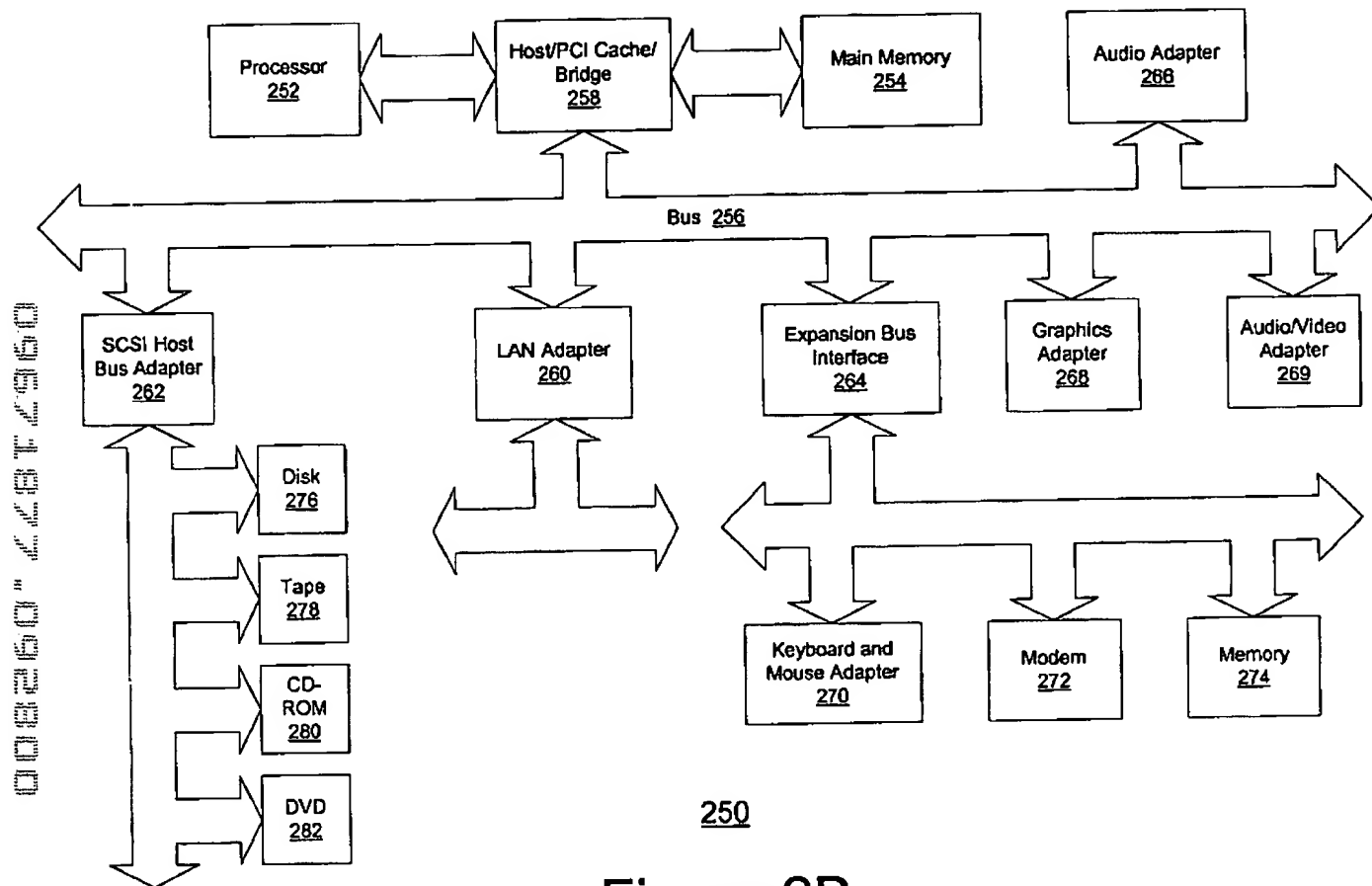


Figure 1
AUS9-2000-0573-US1

The diagram illustrates a computer system 200. At the top, two processors, Processor 202 and Processor 204, are connected to a horizontal System Bus 206. Below the system bus, a Memory Controller/Cache 208 and an I/O Bridge 210 are connected. The I/O Bridge 210 is connected to a vertical I/O Bus 212. The I/O Bus 212 connects to several components: Local Memory 209, a Graphics Adapter 230, a Hard Disk 232, and three PCI Bus Bridges (214, 222, and 224). Each PCI Bus Bridge is connected to a separate PCI Bus (216, 226, and 228). The PCI Bus 216 is also connected to a Modem 218 and a Network Adapter 220.

AUS9-2000-0573-US1



250

Figure 2B

AUS9-2000-0573-US1

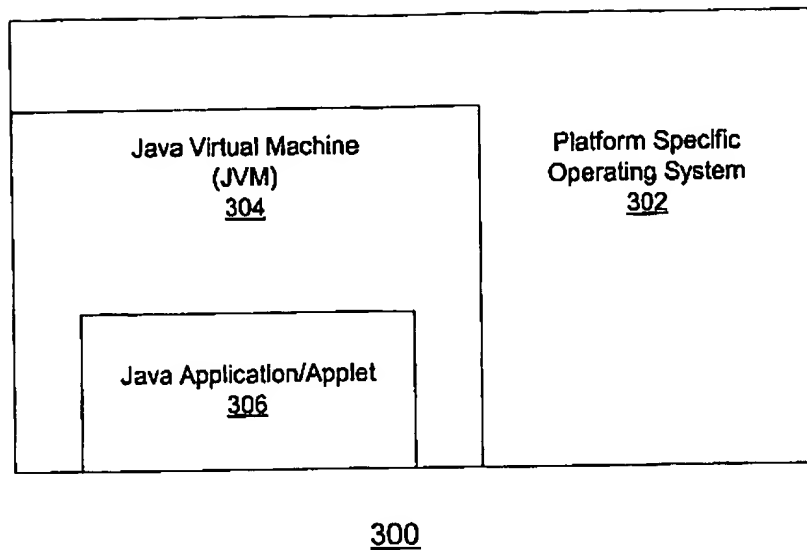


Figure 3A

AUS9-2000-0573-US1

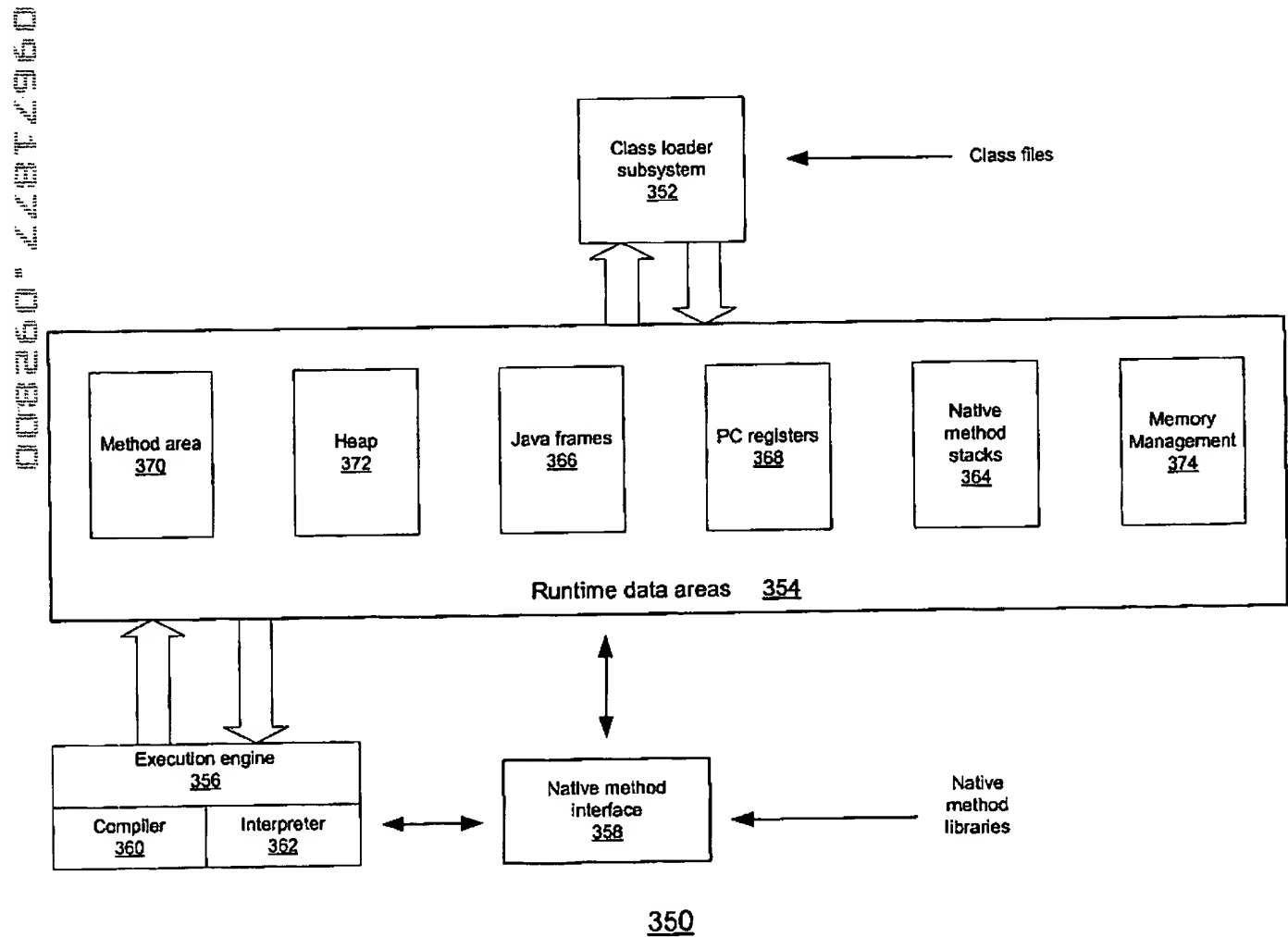


Figure 3B

AUS9-2000-0573-US1

Method Block
400

Address of Return
Stub
410

AUS9-2000-0573-US1

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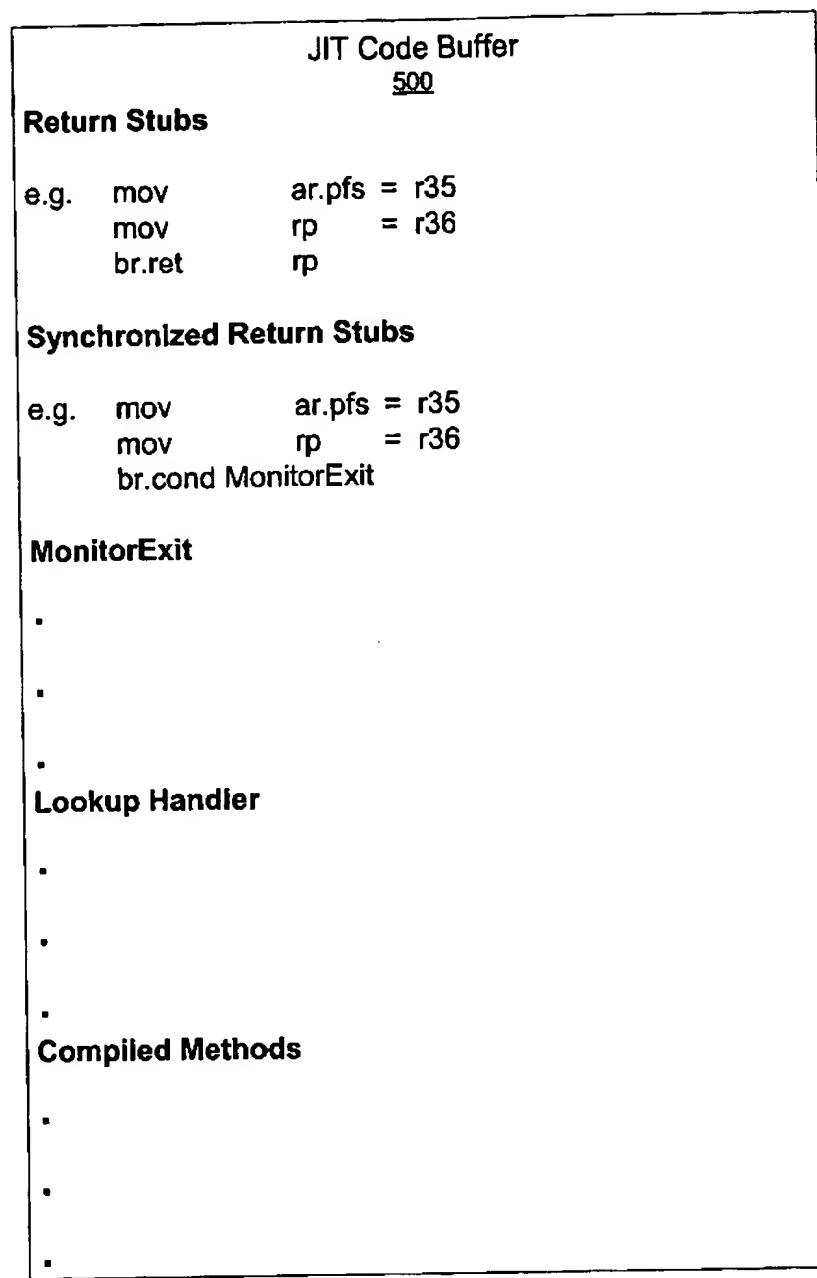
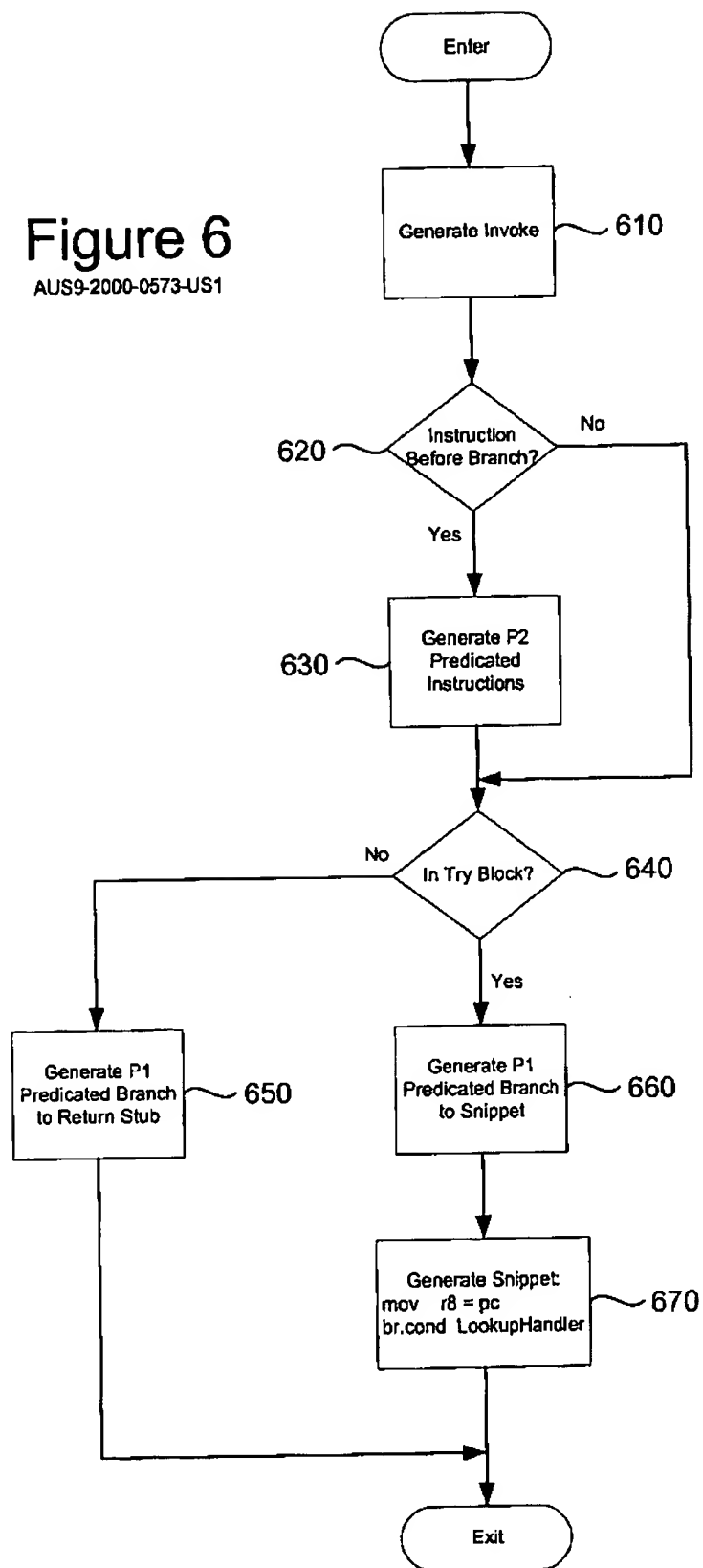


Figure 5
AUS9-2000-0573-US1

Figure 6
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